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ATTY. DOCKET NO.

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SERIAL NO.

New PCT Nat'l Stage  
Application

10/580396

APPLICANT

VON ELBWART, et al.

Alexander GOLITSCHKE EDLER

FILING DATE

May 24, 2006

GROUP

Unassigned

**U.S. PATENT DOCUMENTS**

| EXAMINER<br>INITIAL | DOCUMENT NUMBER | DATE | NAME | CLASS | SUBCLASS | FILING DATE<br>IF APPROPRIATE |
|---------------------|-----------------|------|------|-------|----------|-------------------------------|
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**FOREIGN PATENT DOCUMENTS**

| DOCUMENT NUMBER | DATE    | COUNTRY | CLASS | SUBCLASS | TRANSLATION |
|-----------------|---------|---------|-------|----------|-------------|
|                 |         |         |       |          | YES NO      |
| 2 3 1 0 1 8 6   | 12/2001 | CA      |       |          |             |
| 0 3 0 3 2 4 9 9 | 04/2003 | WO      |       |          |             |
| 0 2 3 7 7 3 1   | 05/2002 | WO      |       |          |             |
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**OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)**

PCT International Search Report dated March 11, 2005.

A. Nauh, et al., "Bootstrap Decoding of Low-Density Parity-Check Codes," IEEE Communications Letters, IEEE Services Center, vol. 6, no. 9, XP001128483, pages 391-393, Sept. 2002.

Y. Mao, et al., "Decoding Low-Density Parity-Check Codes with Probabilistic Scheduling," IEEE Communications Letters, IEEE Service Center, vol. 5, no. 10, XP001110789, pages 414-416, Oct. 2001.

L. Yin, et al., "Modified Belief-Propagation Algorithm for Decoding of Irregular Low-Density Parity-Check Codes," Electronics Letters, IEE Stevenage, vol. 38, no. 24, XP006019345, pages 1551-1553, Nov. 21, 2002.

F. R. Kschischang, et al., "Factor Graphs and the Sum-Product Algorithm," IEEE Transactions on Information Theory, IEEE Inc., vol. 47, no. 2, XP001009659, pages 498-519, Feb. 2001.

**EXAMINER:** Initial if citation is considered, draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

(Form PTO-1449 [6-4])

/Shelly A Chase /

09/07/2010

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